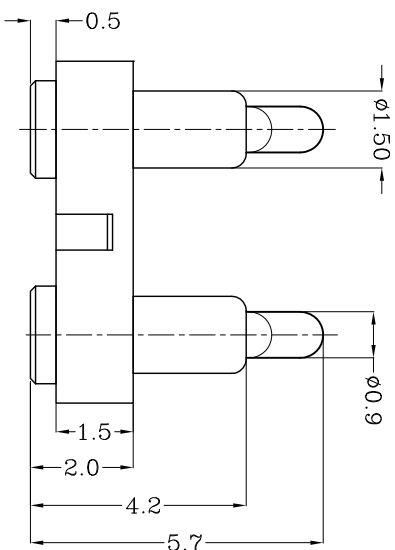
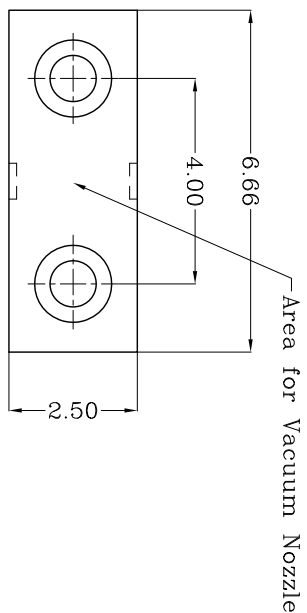


Recommended PCB Layout

USED ON

Plating Plunger: Min 0.4 μ m Au over 1.4 μ m Ni
Plating Body: Min 0.1 μ m Au over 1.4 μ m .

DIMENSIONS IN MM		MATERIAL		HT TR		FIN	
TOLERANCES UNLESS OTHER SPECIFIED		1		—		V	
0	= ±—	DWN	APVD				
0.0	= ±0.15	qin	lanling	<i>Top-Link</i>		Top-Link	
0.00	= ±0.10	CHK	REL	<i>Electronics</i>		Electronics Ltd	
0.000	= ±—	Jlm					
ANGLE: ±1°							
SURF TEXTURE: 32/ V		NAME BATTERY POGO CONNECTOR					
REMOVE BURRS, BREAK SHARP EDGES R0.05 MAX		2 POSITIONS PGC-15-5-7-R-2P-4.0PH					
DO NOT SCALE PRINT		SCALE	A4	DWG NO.	C-100357	SHEET 1 OF 1	REV A
		1:1					

100357-2
PART NUMBER